

Fei Yuan

CMOS Circuits for Passive Wireless Microsystems

Cmos Circuits For Passive Wireless Microsystems

Fei Yuan



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CMOS Circuits for Passive Wireless Microsystems Fei Yuan, 2010-10-28 This book provides a comprehensive treatment of CMOS circuits for passive wireless microsystems Major topics include an overview of passive wireless microsystems design challenges of passive wireless microsystems fundamental issues of ultra low power wireless communications radio frequency power harvesting ultra low power modulators and demodulators ultra low power temperature compensated current and voltage references clock generation and remote calibration and advanced design techniques for ultra low power analog signal processing *CMOS Time-Mode Circuits and Systems* Fei Yuan, 2018-09-03 Time mode circuits where information is represented by time difference between digital events offer a viable and technology friendly means to realize mixed mode circuits and systems in nanometer complementary metal oxide semiconductor CMOS technologies Various architectures of time based signal processing and design techniques of CMOS time mode circuits have emerged however an in depth examination of the principles of time based signal processing and design techniques of time mode circuits has not been available until now CMOS Time Mode Circuits and Systems Fundamentals and Applications is the first book to deliver a comprehensive treatment of CMOS time mode circuits and systems Featuring contributions from leading experts this authoritative text contains a rich collection of literature on time mode circuits and systems The book begins by presenting a critical comparison of voltage mode current mode and time mode signaling for mixed mode signal processing and then Covers the fundamentals of time mode signal processing such as voltage to time converters all digital phase locked loops and frequency synthesizers Investigates the performance characteristics architecture design techniques and implementation of time to digital converters Discusses time mode delta sigma based analog to digital converters placing a great emphasis on time mode quantizers Includes a detailed study of ultra low power integrated time mode temperature measurement systems CMOS Time Mode Circuits and Systems Fundamentals and Applications provides a valuable reference for circuit design engineers hardware system engineers graduate students and others seeking to master this fast evolving field *Wireless Information and Power Transfer: A New Paradigm for Green Communications* Dushantha Nalin K. Jayakody, John Thompson, Symeon Chatzinotas, Salman Durrani, 2017-07-20 This book presents breakthroughs in the design of Wireless Energy Harvesting WEH networks It bridges the gap between WEH through radio waves communications and power transfer which have largely been designed separately The authors present an overview of the RF EHNs including system architecture and RF energy harvesting techniques and existing applications They also cover the idea of WEH in novel discoveries of information the theoretical bounds in WEH wireless sensor networks usage of modern channel coding together with WEH energy efficient resource allocation mechanisms distributed self organized energy efficient designs delay energy trade off specific protocols for energy efficient communication designs D2D communication and energy efficiency cooperative wireless networks and cognitive networks CMOSET 2014: Abstracts CMOS Emerging Technologies Research, 2014-03-26

A collection of abstracts for talks presented at the 2014 CMOS Emerging Technologies Research Symposium in Grenoble France July 6 8 2014 The CMOS Emerging Technologies Research Symposium is a research and business event for those who want to discuss and find out about new exciting high tech opportunities The conference provides researchers companies and academic institutions with a platform for showcasing their technology innovations products and services By bringing together people from all areas of the high tech arena we create a stimulating common ground for exploring collaborations and encouraging discussions on emerging technologies

Low Power Circuits for Emerging Applications in Communications, Computing, and Sensing Fei Yuan, 2018-12-07 The book addresses the need to investigate new approaches to lower energy requirement in multiple application areas and serves as a guide into emerging circuit technologies It explores revolutionary device concepts sensors and associated circuits and architectures that will greatly extend the practical engineering limits of energy efficient computation The book responds to the need to develop disruptive new system architectures circuit microarchitectures and attendant device and interconnect technology aimed at achieving the highest level of computational energy efficiency for general purpose computing systems Features Discusses unique technologies and material only available in specialized journal and conferences Covers emerging applications areas such as ultra low power communications emerging bio electronics and operation in extreme environments Explores broad circuit operation ex analog RF memory and digital circuits Contains practical applications in the engineering field as well as graduate studies Written by international experts from both academia and industry

Electronics Michael Olorunfunmi Kolawole, 2020-06-15 This book gives clear explanations of the technical aspects of electronics engineering from basic classical device formulations to the use of nanotechnology to develop efficient quantum electronic systems As well as being up to date this book provides a broader range of topics than found in many other electronics books This book is written in a clear accessible style and covers topics in a comprehensive manner This book's approach is strongly application based with key mathematical techniques introduced helpful examples used to illustrate the design procedures and case studies provided where appropriate By including the fundamentals as well as more advanced techniques the author has produced an up to date reference that meets the requirements of electronics and communications students and professional engineers Features Discusses formulation and classification of integrated circuits Develops a hierarchical structure of functional logic blocks to build more complex digital logic circuits Outlines the structure of transistors bipolar JFET MOSFET or MOS CMOS their processing techniques their arrangement forming logic gates and digital circuits optimal pass transistor stages of buffered chain sources and types of noise and performance of designed circuits under noisy conditions Explains data conversion processes choice of the converter types and inherent errors Describes electronic properties of nanomaterials the crystallites size reduction effect and the principles of nanoscale structure fabrication Outlines the principles of quantum electronics leading to the development of lasers masers reversible quantum gates and circuits and applications of quantum cells and fabrication methods including self

assembly quantum dot cellular automata and tunneling superconducting circuits and describes quantum error correction techniques Problems are provided at the end of each chapter to challenge the reader's understanding

Injection-Locking in Mixed-Mode Signal Processing Fei Yuan, 2019-05-17 This book provides readers with a comprehensive treatment of the principles circuit design techniques and applications of injection locking in mixed mode signal processing with an emphasis on CMOS implementation Major topics include An overview of injection locking the principle of injection locking in harmonic and non harmonic oscillators lock range enhancement techniques for harmonic oscillators lock range enhancement techniques for non harmonic oscillators and the emerging applications of injection locking in mixed mode signal processing Provides a single source reference to the principles circuit design techniques and applications of injection locking in mixed mode signal processing Includes a rich collection of design techniques for increasing the lock range of oscillators under injection along with in depth examination of the pros and cons of these methods Enables a broad range of applications such as passive wireless microsystems forwarded clock parallel data links frequency synthesizers for wireless and wireline communications and low phase noise phase locked loops

CMOSET 2014 Vol. 2: Circuits and Systems Track CMOS Emerging Technologies Research, 2014-06-19 Presentation slides from the Plenary track at the 2014 CMOS Emerging Technologies Research conference in Grenoble France

Microwave Systems and Applications Sotirios Goudos, 2017-01-11 Microwave systems are key components of every modern wireless communication system The main objective of this book was to collect as many different state of the art studies as possible in order to cover in a single volume the main aspects of microwave systems and applications This book contains 17 chapters written by acknowledged experts researchers academics and microwave engineers providing comprehensive information and covering a wide range of topics on all aspects of microwave systems and applications This book is divided into four parts The first part is devoted to microwave components The second part deals with microwave ICs and innovative techniques for on chip antenna design The third part presents antenna design cases for microwave systems Finally the last part covers different applications of microwave systems

Complementary Metal Oxide Semiconductor Kim Ho Yeap, Humaira Nisar, 2018-08-01 In this book Complementary Metal Oxide Semiconductor CMOS devices are extensively discussed The topics encompass the technology advancement in the fabrication process of metal oxide semiconductor field effect transistors or MOSFETs which are the fundamental building blocks of CMOS devices and the applications of transistors in the present and future eras The book is intended to provide information on the latest technology development of CMOS to researchers physicists as well as engineers working in the field of semiconductor transistor manufacturing and design

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