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Designing TSVs for 3D Integrated Circuits

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Nauman Khan, Soha Hassoun



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Electrical Design of Through Silicon Via Manho Lee, Jun So Pak, Joungho Kim, 2014-05-11 Through Silicon Via TSV is a key technology for realizing three dimensional integrated circuits 3D ICs for future high performance and low power systems with small form factors This book covers both qualitative and quantitative approaches to give insights of modeling TSV in a various viewpoints such as signal integrity power integrity and thermal integrity Most of the analysis in this book includes simulations numerical modelings and measurements for verification The author and co authors in each chapter have studied deep into TSV for many years and the accumulated technical know hows and tips for related subjects are comprehensively covered

Arbitrary Modeling of Tsvs for 3D Integrated Circuits Khaled Salah, Yehea Ismail, Alaa El-Rouby, 2014-09-30

Design-for-Test and Test Optimization Techniques for TSV-based 3D Stacked ICs Brandon Noia, Krishnendu Chakrabarty, 2013-11-19 This book describes innovative techniques to address the testing needs of 3D stacked integrated circuits ICs that utilize through silicon

vias TSVs as vertical interconnects The authors identify the key challenges facing 3D IC testing and present results that have emerged from cutting edge research in this domain Coverage includes topics ranging from die level wrappers self test circuits and TSV probing to test architecture design test scheduling and optimization Readers will benefit from an in depth look at test technology solutions that are needed to make 3D ICs a reality and commercially viable

Design for High Performance, Low Power, and Reliable 3D Integrated Circuits Sung Kyu Lim,2012-11-27 This book provides readers with a variety of algorithms and software tools dedicated to the physical design of through silicon via TSV based three dimensional integrated circuits It describes numerous manufacturing ready GDSII level layouts of TSV based 3D ICs developed with the tools covered in the book This book will also feature sign off level analysis of timing power signal integrity and thermal analysis for 3D IC designs Full details of the related algorithms will be provided so that the readers will be able not only to grasp the core mechanics of the physical design tools but also to be able to reproduce and improve upon the results themselves This book will also offer various design for manufacturability DFM design for reliability DFR and design for testability DFT techniques that are considered critical to the physical design process

Design and Modeling for 3D ICs and Interposers Madhavan Swaminathan,2013-11-05 3D Integration is being touted as the next semiconductor revolution This book provides a comprehensive coverage on the design and modeling aspects of 3D integration in particularly focus on its electrical behavior Looking from the perspective the Silicon Via TSV and Glass Via TGV technology the book introduces 3DICs and Interposers as a technology and presents its application in numerical modeling signal integrity power integrity and thermal integrity The authors underscored the potential of this technology in design exchange formats and power distribution

Data Bus Swizzling in Through Silicon Via Based Three Dimensional Integrated Circuits Shen Ge,2012

3D Integration in VLSI Circuits Katsuyuki Sakuma,2018-04-17 Currently the term 3D integration includes a wide variety of different integration methods such as 2.5 dimensional 2.5D interposer based integration 3D integrated circuits 3D ICs 3D systems in package SiP 3D heterogeneous integration and monolithic 3D ICs The goal of this book is to provide readers with an understanding of the latest challenges and issues in 3D integration TSVs are not the only technology element needed for 3D integration There are numerous other key enabling technologies required for 3D integration and the speed of the development in this emerging field is very rapid To provide readers with state of the art information on 3D integration research and technology developments each chapter has been contributed by some of the world's leading scientists and experts from academia research institutes and industry from around the globe Covers chip wafer level 3D integration technology memory stacking reconfigurable 3D and monolithic 3D IC Discusses the use of silicon interposer and organic interposer Presents architecture design and technology implementations for 3D FPGA integration Describes oxide bonding Cu/SiO₂ hybrid bonding adhesive bonding and solder bonding Addresses the issue of thermal dissipation in 3D integration

Computer-Aided Design and VLSI Device Development Kit Man Cham,Soo-Young Oh,John L.

Moll,2013-12-19 This book is concerned with the use of Computer Aided Design CAD in the device and process development of Very Large Scale Integrated Circuits VLSI The emphasis is in Metal Oxide Semiconductor MOS technology State of the art device and process development are presented This book is intended as a reference for engineers involved in VLSI development who have to solve many device and process problems CAD specialists will also find this book useful since it discusses the organization of the simulation system and also presents many case studies where the user applies the CAD tools in different situations This book is also intended as a text or reference for graduate students in the field of integrated circuit fabrication Major areas of device physics and processing are described and illustrated with Simulations The material in this book is a result of several years of work on the implementation of the simulation system the refinement of physical models in the simulation programs and the application of the programs to many cases of device developments The text began as publications in journals and conference proceedings as well as lecture notes for a Hewlett Packard internal CAD course This book consists of two parts It begins with an overview of the status of CAD in VLSI which points out why CAD is essential in VLSI development Part A presents the organization of the two dimensional simulation system

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