

Fourth Edition CMOS VLSI Design

A CIRCUITS AND SYSTEMS PERSPECTIVE

Neil H. E. Weste | David Money Harris

The fourth edition of this best-selling text discusses modern techniques for the design of complex and high-performance CMOS systems on a chip. Covering the fundamentals of CMOS design from the digital systems level to the circuit level, this book is a guide to good design practices.

FEATURES

- Broad, up-to-date, and comprehensive coverage of the entire field of CMOS VLSI design
- Introduces each key element of VLSI design, including design, power, interconnect, and robustness
- Ample circuit-level coverage, emphasizing practical circuits used in commercial chips
- Illustrates circuit simulation with SPICE through a complete tutorial chapter (Chapter 11)
- Presents extensive coverage of data path, array, and special purpose building blocks (Chapters 11–13)
- Contains a rich set of problems, worked examples and exercises for learning reinforcement
- Presents “war stories” of “chips gone bad” and their lessons for today’s designers
- Links theory to practice through expert Historical Perspectives and Pitfall sections that reveal what’s happening in real VLSI and engineering laboratories

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Cover image: Intel Pentium 4 processor



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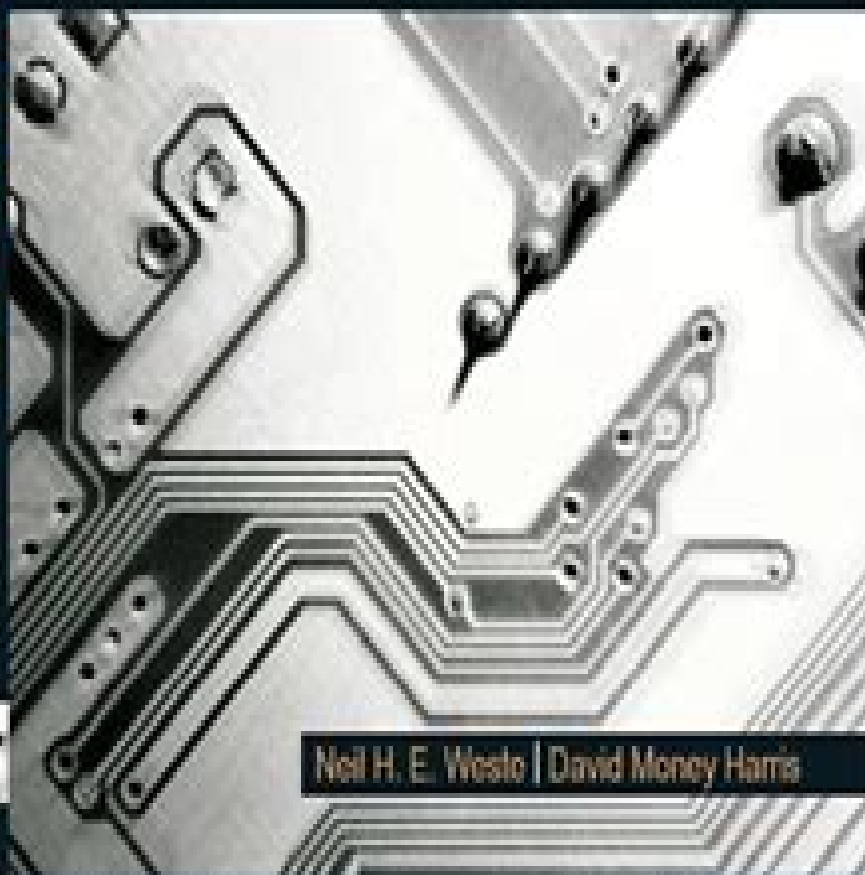
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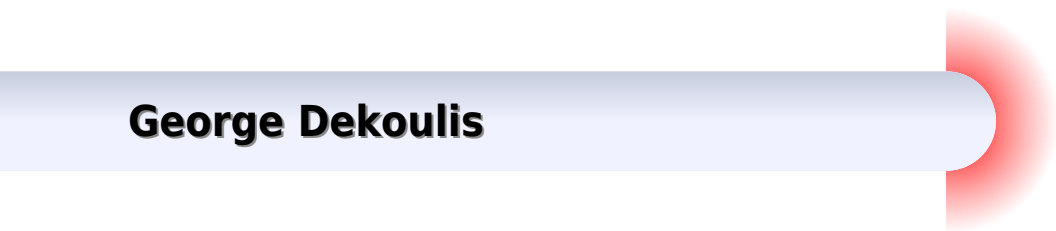


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Field George Dekoulis, 2017-05-31 This edited volume Field Programmable Gate Array is a collection of reviewed and relevant research chapters offering a comprehensive overview of recent developments in the field of semiconductors The book comprises single chapters authored by various researchers and edited by an expert active in the aerospace engineering systems research area All chapters are complete within themselves but united under a common research study topic This publication aims at providing a thorough overview of the latest research efforts by international authors and open new possible research paths for further novel developments

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High Performance Embedded Computing Handbook David R. Martinez, Robert A. Bond, M. Michael Vai, 2018-10-03 Over the past several decades applications permeated by advances in digital signal processing have undergone unprecedented growth in capabilities The editors and authors of High Performance Embedded Computing Handbook A Systems Perspective have been significant contributors to this field and the principles and techniques presented in the handbook are reinforced by examples drawn from their work The chapters cover system components found in today s HPEC systems by addressing design trade offs implementation options and techniques of the trade then solidifying the concepts with specific HPEC system examples This approach provides a more valuable learning tool Because readers learn about these subject areas through factual implementation cases drawn from the contributing authors own experiences Discussions include Key subsystems and components Computational characteristics of high performance embedded algorithms and applications Front end real time processor technologies such as analog to digital conversion

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CMOS Test and Evaluation Manjul Bhushan, Mark B. Ketchen, 2014-12-03 CMOS Test and Evaluation A Physical Perspective is a single source for an integrated view of test and data analysis methodology for CMOS products covering circuit sensitivities to MOSFET characteristics impact of silicon technology process variability applications of embedded test structures and sensors product yield and reliability over the lifetime of the product This book also covers statistical data analysis and visualization techniques test equipment and CMOS product specifications and examines product behavior over its full voltage temperature and frequency range **Carbon Nanotubes** Prasanta Ghosh, Arti Rushi, Kunal Datta, 2021-10-13 Carbon Nanotubes Redefining the World of Electronics is a compendium of current state of the art information about carbon nanotubes CNTs and their potential applications in electronics Chapters cover such topics as the incorporation of CNTs into electronic devices CNT based rubber composites for electronic components the role of CNTs in different energy storage and conversion systems and ternary implementations of carbon nanotube field effect transistor CNTFET circuits CMOS VLSI Design Neil H. E. Weste, David Money Harris, 2011 For both introductory and advanced courses in VLSI design this authoritative comprehensive textbook is highly accessible to beginners yet offers unparalleled breadth and depth for more experienced readers The Fourth Edition of CMOS VLSI Design A Circuits and Systems perspective presents broad and in

depth coverage of the entire field of modern CMOS VLSI Design The authors draw upon extensive industry and classroom experience to introduce today s most advanced and effective chip design practices They present extensively updated coverage of every key element of VLSI design and illuminate the latest design challenges with 65 nm process examples This book contains unsurpassed circuit level coverage as well as a rich set of problems and worked examples that provide deep practical insight to readers at all levels

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On-Line Testing for VLSI Michael Nicolaidis, Yervant Zorian, Dhiraj Pradhan, 2013-03-09 Test functions fault detection diagnosis error correction repair etc that are applied concurrently while the system continues its intended function are defined as on line testing In its expanded scope on line testing includes the design of concurrent error checking subsystems that can be themselves self checking fail safe systems that continue to function correctly even after an error occurs reliability monitoring and self test and fault tolerant designs On Line Testing for VLSI contains a selected set of articles that discuss many of the modern aspects of on line testing as faced today The contributions are largely derived from recent IEEE International On Line Testing Workshops Guest editors Michael Nicolaidis Yervant Zorian and Dhiraj Pradhan organized the articles into six chapters In the first chapter the editors introduce a large number of approaches with an expanded bibliography in which some references date back to the sixties On Line Testing for VLSI is an edited volume of original research comprising invited contributions by leading researchers

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